



Docket No.: 1381.1005

#5 dated 9/17/02
RECEIVED
TECHNOLOGY CENTER 2800
SEP 12 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Shunji BABA et al.

Serial No. 09/753,570

Group Art Unit: 2811

Confirmation No. 2008

Filed: January 4, 2001

Examiner:

For: **METHOD OF MOUNTING CHIP ONTO PRINTED CIRCUIT BOARD IN SHORTENED WORKING TIME**

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action mailed August 13, 2002, applicants elects Claims 1, 2 and 5-14 of Group II, without traverse.

IN THE CLAIMS

Please CANCEL claims 3, 4 and 15-28, without prejudice.

Respectfully submitted,

STAAS & HALSEY LLP

Date: September 11, 2002

By: 

H.J. Staas
Registration No. 22,010

700 11th Street, N.W., Ste. 500
Washington, D.C. 20001
(202) 434-1500